

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3460995	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:16
L2	10	1 and active adj2 (region or area) with (three or "3") adj3 (word or wordline) and (three or "3") adj3 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:22
L3	7	2 and cell with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:19
L4	23	1 and active adj2 (region or area) with straight with adjacent	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:24
L5	8	1 and active adj2 (region or area) with straight with adjacent near3 (word or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:25
L6	6	5 and cell with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:26
L7	6	6 and capacitor near3 contact	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:27
L8	6	7 and (bit or bitline) adj3 contact	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:28
L9	1	7 and (bit or bitline) adj3 contact with sandwich\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:28
L10	397	1 and second adj row adj decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:29
L11	1	10 and cell with second adj (switch or transistor) with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:30

## EAST Search History

L12	17	10 and second adj column adj decoder with row adj3 address	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:31
-----	----	---	---	----	----	------------------

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20	((memory or storage) and active adj region and (three or "3") and (word or wordline) and (bit or bitline) and capacitor adj contact and bit adj3 contact).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:37
L3	5	((memory or storage) and active adj region and (three or "3") and (word or wordline) and (bit or bitline) and capacitor adj contact and bit adj3 contact and sandwich\$5).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:40
L4	2	((memory or storage) and second adj row adj decoder and second adj column adj decoder and cell and second adj (transistor or switch\$5) and (word or wordline) and (bit or bitline) and row adj address).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/08 18:42